[METHOD AND APPARATUS FOR PERFORMING INPUT/OUTPUT FLOOR PLANNING ON AN INTEGRATED CIRCUIT DESIGN]

Abstract

A method for performing input/output (I/O) floor planning on an integrated circuit design is disclosed. User design data related to I/O circuit associated with each package pin is initially collected. The collected user design data is then sorted according to operating conditions. Next, an I/O behavioral model and a package model are chosen based on the sorted data. A simulation deck is dynamically built with appropriate operating conditions. Finally, a simulation is performed through a circuit simulator using the chosen I/O behavioral model and the operating conditions.